# SK6812RGBX-XX SPECIFICATION INTEGRATED LIGHT SOURCE INTELLIGENT CONTROL OF CHIP-ON-TOP SMD TYPE LED

- Document No.: SPC/ SK6812RGBX-XX
- Model No.: SK6812RGBX-XX
- Description: 5.5x5.0x1.6mm Top SMD Type 0.25 Watt Power tegrated light source Intelligent control LED
- Rev. No.: 02
- Date: 2015-08-20



RoHS



# INTEGRATED LIGHT SOURCE INTELLIGENT CONTROL OF CHIP-ON-TOP SMD TYPE LED Model: SK6812RGBX-XX

#### 1. Product Overview :

SK6812RGBX-XX is a smart LED control circuit and light emitting circuit in one controlled LED source, which has the shape of a 5050 LED chip. Each lighting element is a pixel, and the intensities of the pixels are contained within the intelligent digital interface input. The output is driven by patented PWM technology, which effectively guarantees high consistency of the color of the pixels. The control circuit consists of a signal shaping amplification circuit, a built-in constant current circuit, and a high precision RC oscillator.

The data protocol being used is unipolar NRZ communication mode. The 32-bit data is transmitted from the controller to DIN of the first element, and if it is accepted it is extracted pixel to pixel. After an internal data latch, the remaining data is passed through the internal amplification circuit and sent out on the DO port to the remaining pixels. The pixel is reset after the end of DIN. Using automatic shaping forwarding technology makes the number of cascaded pixels without signal transmission only limited by signal transmission speed.

The LED has a low driving voltage (which allows for environmental protection and energy saving), high brightness, scattering angle, good consistency, low power, and long life. The control circuit is integrated in the LED above.

#### 2. Main Application Field:

• Full color LED string light, LED full color module, LED super hard and soft lights, LED guardrail tube, LED appearance / scene lighting

• LED point light, LED pixel screen, LED shaped screen, a variety of electronic products, electrical equipment etc..

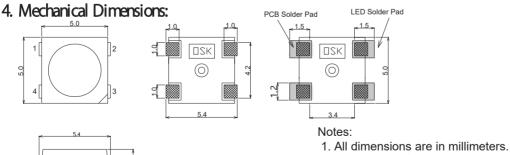
#### 3. Description:

• Top SMD internal integrated high quality external control line serial cascade constant current IC;

• control circuit and the chip in SMD 5050 components, to form a complete control of pixel, color mixing uniformity and consistency;

•built-in data shaping circuit, a pixel signal is received after wave shaping and output waveform distortion will not guarantee a line;

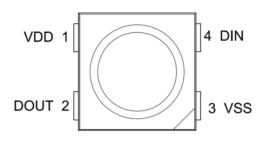
- •The built-in power on reset and reset circuit, the power does not work;
- •gray level adjusting circuit (256 level gray scale adjustable);
- red drive special treatment, color balance;
- line data transmission;
- plastic forward strengthening technology, the transmission distance between two points over 10M;
- OUsing a typical data transmission frequency of 800 Kbps, when the refresh rate of 30 frames per sec



2. Tolerance is ±0.1mm unless otherwise noted

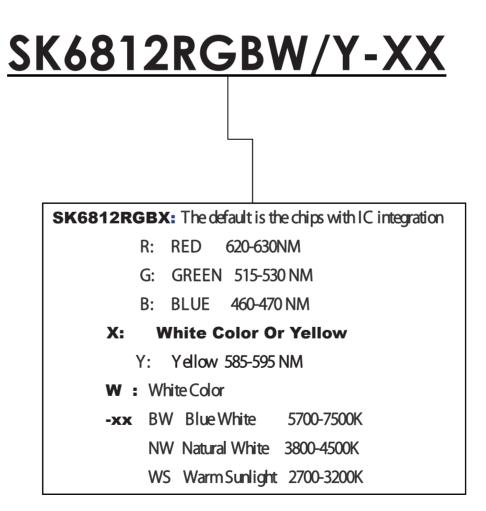


### 5. PIN configuration



| NO. | Symbol | Function description       |  |  |  |  |
|-----|--------|----------------------------|--|--|--|--|
| 1   | VDD    | Power supply LED           |  |  |  |  |
| 2   | DOUT   | Control data signal output |  |  |  |  |
| 3   | VSS    | Ground                     |  |  |  |  |
| 4   | DIN    | Control data signal input  |  |  |  |  |

6. General Information





| Parameter            | Symbol           | Range        | Unit |
|----------------------|------------------|--------------|------|
| Power supply voltage | VDD              | +3.5~+5.5    | V    |
| Logic input voltage  | V <sub>IN</sub>  | -0.5~VDD+0.5 | V    |
| Working temperature  | Topt             | -40~+85      | °C   |
| Storage temperature  | Tstg             | -50~+150     | °C   |
| ESD pressure         | V <sub>ESD</sub> | 4K           | V    |

# 7. Absolute Maximum Ratings (Ta=25°C,VSS=0V) :

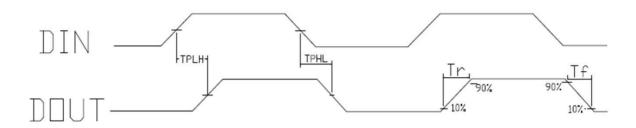
# 8. The electrical parameters (unless otherwise specified, TA=-20 ~ +70 $^{\circ}$ C, VDD=4.5 ~ 5.5V, VSS=0V):

| Parmeter                   | Symbol      | Min | Typical | Max | Unit   | Test conditions                                     |
|----------------------------|-------------|-----|---------|-----|--------|---|
| The chip<br>supply voltage | VDD         |     | 5.2     |     | $\vee$ |   |
| R/G/B port<br>pressure     | VDS,M<br>AX |     |         | 26  | $\vee$ |   |
| DOUT drive                 | IDOH        |     | 49      |     | mA     | DOUT conect ground,<br>the maximum drive<br>current |
| capability                 | IDOL        |     | -50     |     | mA     | DOUT conect +, the<br>largest current               |
| The signal                 | VIH         | 3.4 |         |     | $\vee$ |   |
| input flip<br>threshold    | VIL         |     |         | 1.6 | $\vee$ | VDD=5.0V  |
| The frequency<br>of PWM    | FPWM        |     | 1.2     |     | KHZ    |   |
| Static power consumption   | IDD         |     | 1       |     | mA     |   |

## 9. The dynamic parameters (Ta=25 °C):

| Parameter                         | Symbol | Min | Typical | Max | Unit | Test conditions                   |
|-----------------------------------|--------|-----|---------|-----|------|-----------------------------------|
| The speed of data<br>transmission | fDIN   |     | 800     |     | KHZ  | The duty ratio of 67%<br>(data 1) |
| DOUT transmission                 | TPLH   |     |         | 500 | ns   | DIN→DOUT                          |
| delay                             | TPHL   |     |         | 500 | ns   | DIN→DOOI                          |
| IOUT Rise/Drop                    | Tr     |     |         | 40  | ns   | VDS=1.5                           |
| Time                              | Tf     |     |         | 80  | ns   | IOUT=9mA                          |

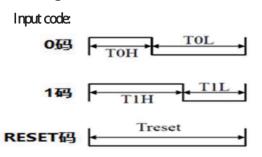




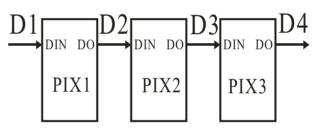
## **10**. The data transmission time (TH+TL=1.25µs±600ns):

| ТОН  | 0 code, high level time    | 0.3µs | $\pm 0.15 \mu s$ |
|------|----------------------------|-------|------------------|
| TOL  | 0 code, low level time     | 0.9µs | $\pm 0.15 \mu s$ |
| ТІН  | 1 code, high level time    | 0.6µs | $\pm 0.15 \mu s$ |
| TIL  | 1 code, low level time     | 0.6µs | $\pm 0.15 \mu s$ |
| Trst | Reset code, low level time | 80µs  |                  |

### 11. Timing waveform



Connection mode:



### 12. The method of data transmission:

|    |                      |               | r            | eset cod | e            |               | reset        | code |
|----|----------------------|---------------|--------------|----------|--------------|---------------|--------------|------|
|    | Data refresh cycle 1 |               |              |          | [            | Data refresh  | cycle 2—     |      |
| D1 | first 32 bit         | second 32 bit | third 32 bit |          | first 32 bit | second 32 bit | third 32 bit |      |
|    |                      |               |              |          |              |               |              |      |
| D2 |                      | second 32 bit | third 32 bit |          |              | second 32 bit | third 32 bit |      |
|    |                      |               |              |          |              |               |              |      |
| D3 |                      |               | third 32 bit | 1        |              |               | third 32 bit |      |
|    |                      |               |              |          |              |               |              |      |
| D4 |                      |               |              |          |              |               |              |      |

Note: the D1 sends data for MCU, D2, D3, D4 for data forwarding automatic shaping cascade circuit.



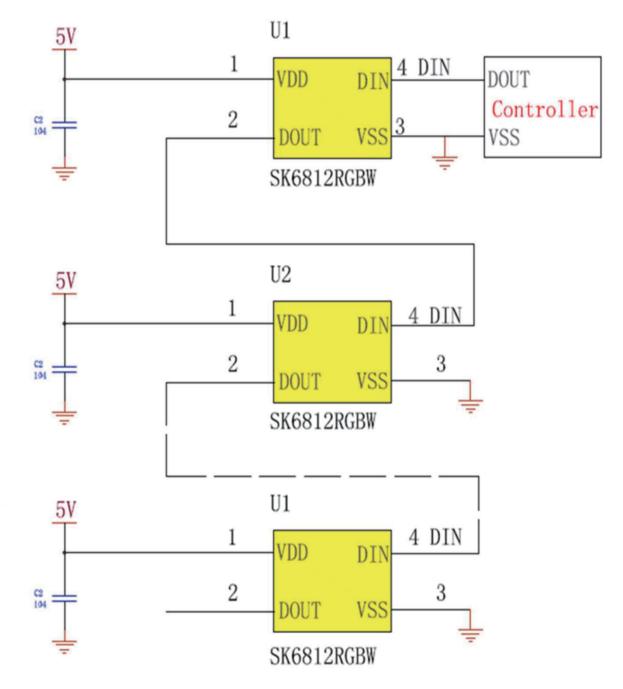


### 13. The data structure of 32bit:

| R7 | R6 | R5 | R4 | R3 | R2 | R1 | RO | G7         | G6 | G5 | G4 |
|----|----|----|----|----|----|----|----|------------|----|----|----|
| G3 | G2 | G1 | G0 | B7 | B6 | B5 | B4 | <b>B</b> 3 | B2 | B1 | BO |
| W7 | W6 | W5 | W4 | W3 | W2 | W1 | WO |            |    |    |    |

Note: high starting, in order to send data (R7 - R6 - ..... ..W0)

# 14. The typical application circuit



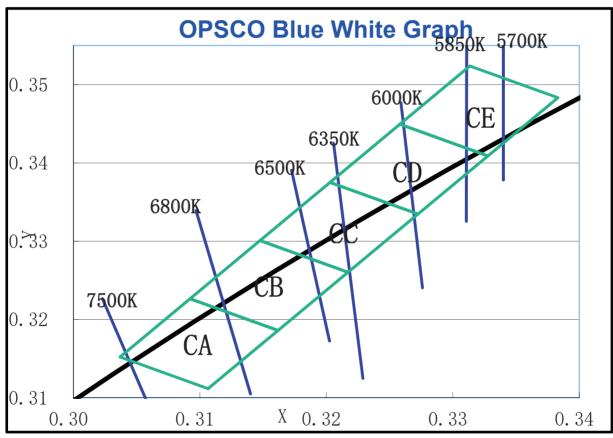


#### 15. White Color Temperature Ranks & CIE Color Rank (Refer to CIE 1931 chromaticity diagram)

| CA | X1        | ¥1       | X2       | ¥2       | Х3       | ¥3       | X4       | ¥4       |
|----|-----------|----------|----------|----------|----------|----------|----------|----------|
| CA | 0. 305649 | 0. 30617 | 0. 29869 | 0. 31022 | 0. 30422 | 0.31765  | 0. 31118 | 0. 3136  |
| СВ | 0. 311181 | 0. 3136  | 0. 30422 | 0. 31765 | 0. 30975 | 0. 32508 | 0. 31671 | 0. 32103 |
| CC | 0. 316713 | 0. 32103 | 0. 30975 | 0. 32508 | 0. 31529 | 0. 33252 | 0. 32225 | 0. 32847 |
| CD | 0. 322245 | 0. 32847 | 0. 31529 | 0. 33252 | 0. 32082 | 0. 33995 | 0. 32778 | 0. 3359  |
| CE | 0. 327777 | 0. 3359  | 0. 32082 | 0. 33995 | 0. 32635 | 0. 34738 | 0. 33331 | 0. 34333 |

CIE chromaticity coordinates (ANSI Cool White)

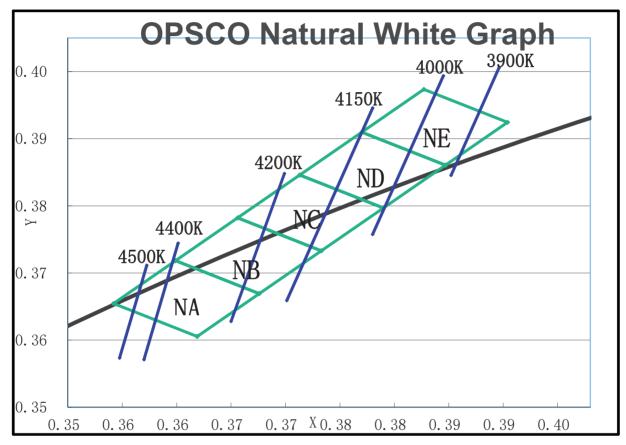
ANSI Blue White Color bin structures





| CA | X1     | Y1     | X2     | Y2     | ХЗ     | ¥3     | X4     | Y4     |
|----|--------|--------|--------|--------|--------|--------|--------|--------|
| NA | 0.3662 | 0.3541 | 0.3578 | 0.3594 | 0.3623 | 0.3666 | 0.3707 | 0.3612 |
| NB | 0.3707 | 0.3612 | 0.3623 | 0.3666 | 0.3669 | 0.3737 | 0.3753 | 0.3683 |
| NC | 0.3753 | 0.3683 | 0.3669 | 0.3737 | 0.3714 | 0.3808 | 0.3798 | 0.3754 |
| ND | 0.3798 | 0.3754 | 0.3714 | 0.3808 | 0.3759 | 0.3879 | 0.3844 | 0.3825 |
| NE | 0.3844 | 0.3825 | 0.3759 | 0.3879 | 0.3805 | 0.3950 | 0.3889 | 0.3897 |

ANSI Natural White Color bin structures

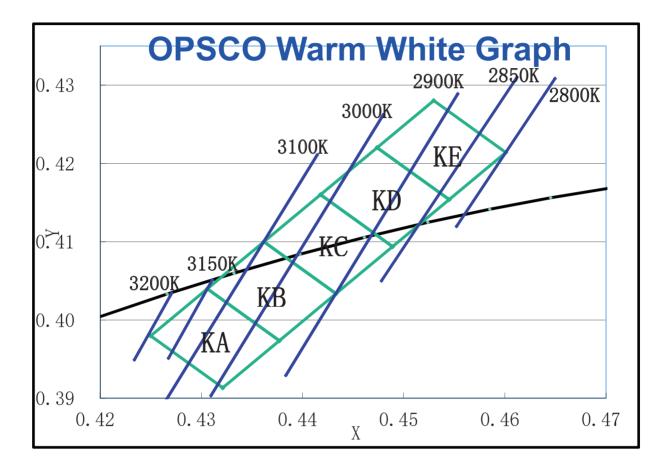




CIE chromaticity coordinates (ANSI Warm White)

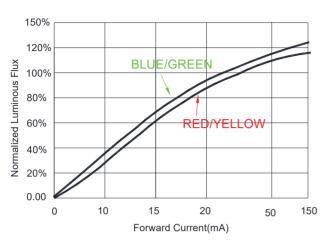
| CA | X1    | ¥1    | X2    | Y2    | Х3    | ¥3    | X4    | Y4    |
|----|-------|-------|-------|-------|-------|-------|-------|-------|
| KA | 0.427 | 0.386 | 0.420 | 0.393 | 0.426 | 0.399 | 0.433 | 0.392 |
| KB | 0.433 | 0.392 | 0.426 | 0.399 | 0.431 | 0.405 | 0.438 | 0.398 |
| KC | 0.438 | 0.398 | 0.431 | 0.405 | 0.437 | 0.411 | 0.444 | 0.404 |
| KD | 0.444 | 0.404 | 0.437 | 0.411 | 0.442 | 0.417 | 0.449 | 0.410 |
| KE | 0.449 | 0.410 | 0.442 | 0.417 | 0.448 | 0.423 | 0.455 | 0.416 |

ANSI Warm White Color bin structures



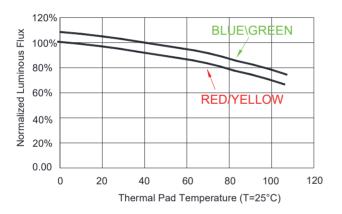


### 16. Standard LED Performance Graph:

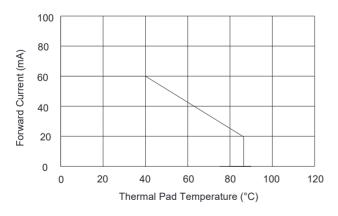


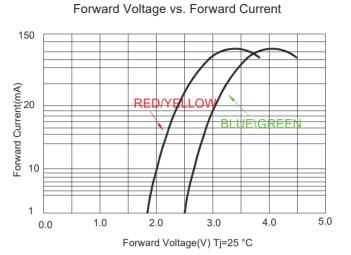
Typical Relative Luminous Flux vs. Forward Current

#### Thermal Pad Temperature vs. Relative Light Output

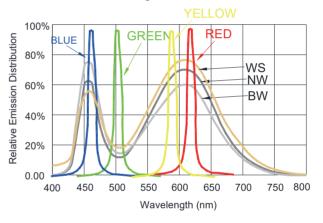


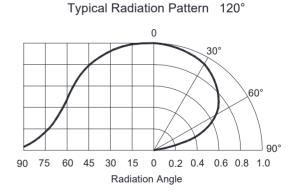
Thermal Pad Temperature vs. Forward Current





Wavelength Characteristics



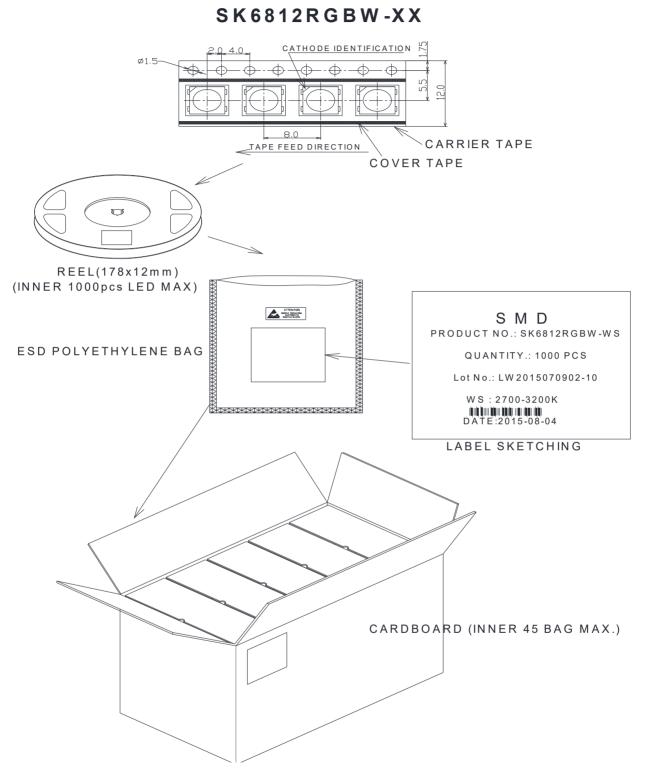


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### 17. Packaging Standard:



The reel pack is applied in SMD LED. The LEDs are packed in cardboard boxes after packaging in normal or antielectrostatic bags. cardboard boxes will be used to protect the LEDs from mechanical shocks during transportation. The boxes are not water resistant and therefore must be kept away from water and moisture.



# **TOP SMD LED Application Notes**

#### 1. Features

The Purposes of making OPSCO's customers and users to have a clear understanding on the ways how to use the LED.

#### 2. Description

Generally. The LED can be used the same way as other general purposed semiconductors. When using OPSCO's TOP SMD LED, the following precautions must be taken to protect the LED.

#### 3. Cautions

#### 3.1. Dust & Cleaning

This emitter has a silicone surface, There are many benefits to the silicone surface in terms of optical properties and improved reliability. However, silicone is a softer material and prone to attract dust. While a minimal amount of dust and debris on the LED will not cause significant reduction in illumination, steps should be taken to keep the emitter free of dust.

These include keeping the LEDs in the manufacturer's package prior to assembly and storing assemblies in an enclosed area after installing the emitters.

Surface condition of this device may change when organic solvents such as trichloroethylene or acetone were applied.

Avoid using organic solvent, it is recommended that isopropyl be used as a solvent for cleaning the LEDs. When using other solvents, it should be confirmed beforehand whether the solvents will dissolve the package and the resin of not.

Do not clean the LEDs by the ultrasonic. When it is absolutely necessary, the influence as ultrasonic cleaning on the LEDs depends on factors such as ultrasonic power. Baking time and assembled condition. Before cleaning, a pre-test should be done to confirm whether any damage to the LEDs will occur.

### 3.2. Moisture Proof Package

In order to avoid the absorption of moisture during transportation and storage, LED are packed in the aluminum envelop, A desiccant is included in the aluminum envelop as it absorbs moisture. When moisture is absorbed into the AMT package it may vaporize and expand during soldering. There is a possibility that this can cause exfoliation of the contacts and damage to the optical characteristics of the LEDs. For this reason, the moisture proof package is used to keep moisture to a minimum in the package.

### 3.3. Storage

In order to avoid the absorption of moisture, It is recommended to store SMD LED (in bulk or taped) in the dry box (or the desiccator) with a desiccant, Otherwise to store them in the following environment as recommended.

a. Temperature: 5°C~30°C b. Humidity: 60% RH Max

It is recommended to solder the LED as soon as possible after unpacking the aluminum envelop, But in case that the LED have to be left unused after unpacking envelop again is requested.

The LED should be soldering within 1 hours after opening the package.

If baking is required, A baking treatment should be performed as follows:

 $70^{\circ}C \pm 5^{\circ}C$  for more than 24 hours.

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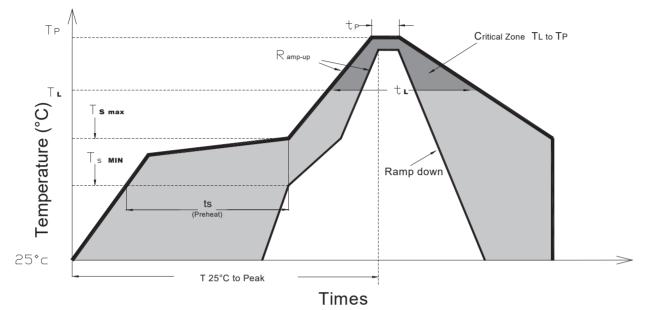
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### 3.4. Reflow Soldering Characteristics

In testing, OPSCO has found S50 LEDs to be compatible with JEDEC J-STD-020C, using the parameters listed below. As a general guideline OPSCO recommends that users follow the recommended soldering profile provided by the manufacturer of solder paste used.

Note that this general guideline is offered as a starting point and may require adjustment for certain PCB designs and

Configurations of reflow soldering equipment.



| Profile Feature   | Lead-Based Solder | Lead-Free Solder |
|---|-------------------|------------------|
| Average Ramp-Up Rate (Ts <sub>max</sub> to Tp )   | 3°C/second max.   | 3°C/second max.  |
| Preheat: Temperature Min (Ts <sub>min</sub> )   | 100°C             | 1 <b>50°</b> C   |
| Preheat: Temperature Min (Ts <sub>max</sub> )   | 1 <b>50℃</b>      | 200°C            |
| Preheat: Time ( ts <sub>min to</sub> ts <sub>max</sub> )                                | 60-120 seconds    | 60-180 seconds   |
| Time Maintained Above: Temperature (T <sub>L</sub> )                                    | 183 °C            | 217 °C           |
| Time Maintained Above: Time (t $_{\rm l}$ )   | 60-150 seconds    | 60-150 seconds   |
| Peak/Classification Temperature (T <sub>P</sub> )                                       | 215 °C            | 240 °C           |
| Time Within 5°C of Actual Peak Temperature ( tp)  | <10 seconds       | <10 seconds      |
| Ramp-Down Rate  | 6°C/second max.   | 6°C/second max.  |
| Time 25 $^\circ \!$ | <6 minutes max.   | <6 minutes max.  |

Note: All temperatures refer to topside of the package, measured on the package body surface.



#### 3.5 Heat Generation:

Thermal design of the end product is of paramount importance. Please consider the heat generation of the LED when making the system design. The coefficient of temperature increase per input electric power is affected by the thermal resistance of the circuit board and density of LED placement on the board, as well as components. It is necessary to avoid in tense heat generation and operate within the maximum rating given in this specification. The operating current should be decided after considering the ambient maximum temperature of LEDs

### 3.6 Electrostatic Discharge & Surge Current:

Electrostatic discharge (ESD) or surge current (EOS) may damage LED.

Precautions such as ESD wrist strap, ESD shoe strap or antistatic gloves must be worn whenever handling of LED.

All devices, equipment and machinery must be properly grounded.

It is recommended to perform electrical test to screen out ESD failures at final inspection.

It is important to eliminate the possibility of surge current during circuitry design.

### 3.7 Moisture Proof Package

Cannot take any responsibility for any trouble that are caused by using the LEDs at conditions exceeding our specifications.

The LED light output is strong enough to injure human eyes. Precautions must be taken to prevent looking directly at the LEDs with unaided eyes for more than a few seconds.

The formal specification must be exchanged and signed by both parties before large volume purchase begins. The appearance and specifications of the product may be modified for improvement without notice.





#### **Change History**

| FCN No. | Date       | Rev. No. | Changes/Reason of changes    |
|---------|------------|----------|------------------------------|
|         | 2015-07-31 | 01       | Initial Document             |
|         | 2015-08-20 | 02       | Modified model and parameter |
|         |            |          |                              |
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| Items       | Signatures | Date       |
|-------------|------------|------------|
| Prepared by | Kevin Zhu  | 2015-08-20 |
| Checked by  |            |            |
| Approved by |            |            |
| FCN#        |            |            |

